



# Vérification, validation et certification des applications embarquées : quid de la robotique ?

Paris, le 22 novembre 2018

# Agenda

---

- ✦ VÉRIFICATION, VALIDATION ET CERTIFICATION
- ✦ QUELQUES CHIFFRES
- ✦ LE MONDE AÉRONAUTIQUE
- ✦ LE MONDE AUTOMOBILE
- ✦ ET LE MONDE ROBOTIQUE ?
- ✦ *UNE CONCLUSION ?*

# Remerciements



*Saadia Dhouib – CEA-LIST*



*Félix Ingrand – LAAS*



*Nicolas Dulac – INTEMPORA*



*Thierry Meslet – BA Systems*



*Herman Bruyninckx – UCL*

# Agenda

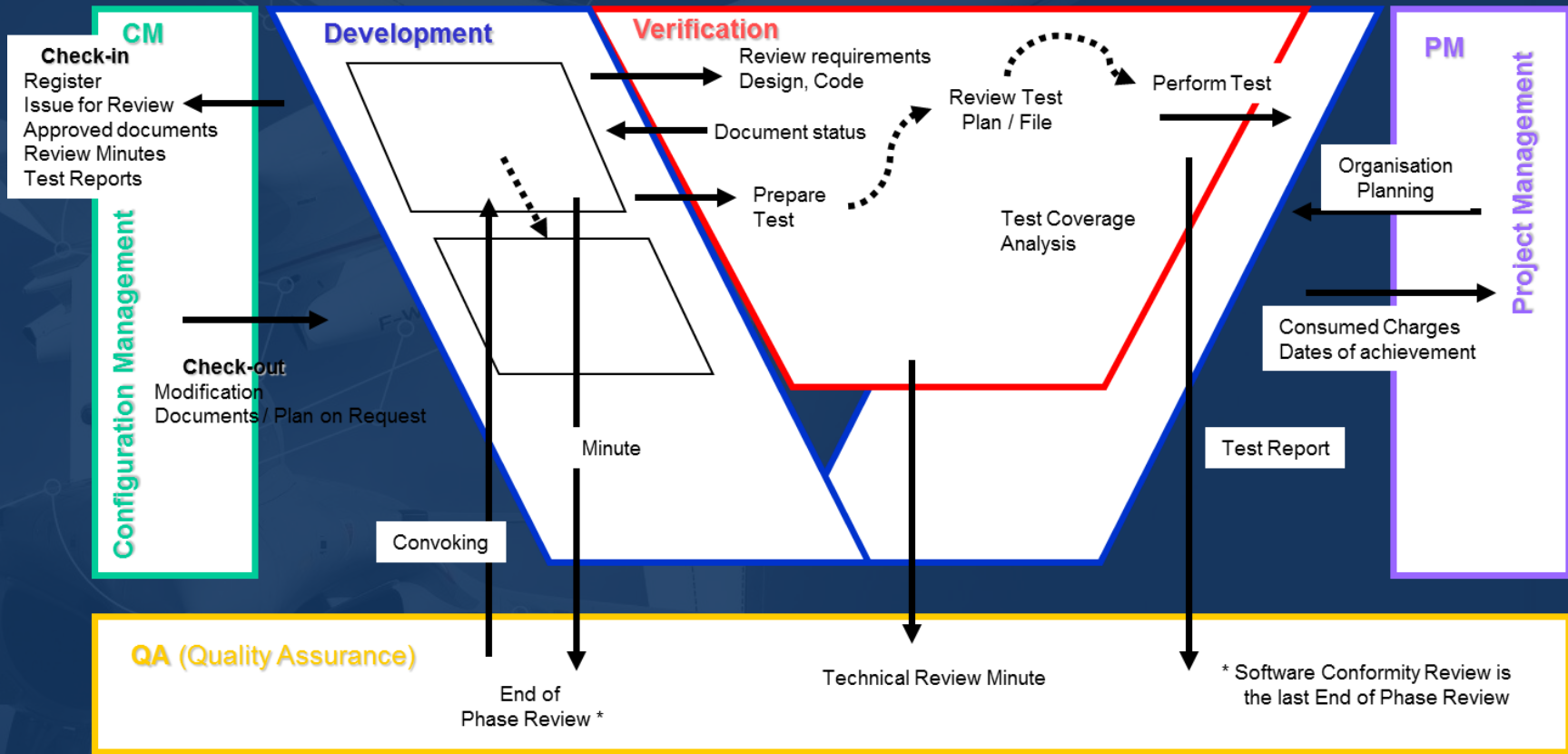
---

- ✦ VÉRIFICATION, VALIDATION ET CERTIFICATION
- ✦ QUELQUES CHIFFRES
- ✦ LE MONDE AÉRONAUTIQUE
- ✦ LE MONDE AUTOMOBILE
- ✦ ET LE MONDE ROBOTIQUE ?
- ✦ *UNE CONCLUSION ?*



# Vérification, Validation et Certification

**C** (Certification liaison process)



- .....> Transition Inside a process: from one activity to another activity
- > Transition from 1 process to another process

# Quelques Informations

	Aéronautique (Airliners, bizjets, GA et RA)	Automobile (dont la voiture « autonome »)	Drones	Robotique Logistique (hors cobot et extérieur*)	Robotique personnelle (hors aspirateurs)
Nombre de machines	~ 40000 airliners	>> 1Million	Qques milliers ↗	Quelques dizaine milliers ↗	Quelques centaines (↗↗?)
Personnes en connexion	Plusieurs centaines	1 dizaine max (hors autocar)	0 Personnes et biens survolés	Principe de ségrégation (cobotique en route)	Quelques personnes
Risque assumé	Airliner , bizjet : $10^{-6}$ / heure de vol GA : $10^{-4}$ / heure de vol	Pas de chiffre global mais constaté $> 10^{-4}$	Pas de chiffres mais volonté $10^{-4}$ pour les plus légers	$10^{-6}$ / heure de fonctionnement (objectif – pas de recueil des incidents)	Pas de chiffres
Autorité de certification / Suivi indépendante	OUI / OUI	NON / EN COURS**	EN COURS / EN COURS	NON / NON	NON / NON
Middleware et OS	RT ARINC ECO A	Ad hoc RT (QNX-like) RTMAPS ROS	Ad hoc Paparazzi OROCOS ROS Xenomai	Ad hoc RT OROCOS (médical) ROS (XP)	Ad hoc (?) OpenRobots OROCOS ROS
Approche « Model Driven »	OUI	EN COURS	NON	NON	INITIATIVES

\* Travail en cours pour la logistique en extérieur mais problème réglementaire délicat

\*\* voir [Etat de Californie](#), et ce [travail universitaire](#).

# Agenda

---

- ✦ VÉRIFICATION, VALIDATION ET CERTIFICATION
- ✦ QUELQUES CHIFFRES
- ✦ LE MONDE AÉRONAUTIQUE
- ✦ LE MONDE AUTOMOBILE
- ✦ ET LE MONDE ROBOTIQUE ?
- ✦ *UNE CONCLUSION ?*

# La vérification et validation du logiciel

## Aéronautique – la DO178C

---

- ✦ ALLOCATION DES ITEMS SOFTWARE À DES NIVEAUX DE QUALITÉ
- ✦ LES NIVEAUX DE QUALITÉ
- ✦ QUELQUES ÉLÉMENTS DEMANDÉS PAR LE PROCESSUS DE DÉVELOPPEMENT



# The software IDAL allocation process

- ✦ At system level, there are the system development process and the associated safety analysis process that identifies the failure conditions and their effects. The system is decomposed in items that are allocated to an IDAL\*.
- ✦ The software items reliability is process based because it is considered too difficult to assess the software reliability *(Extract from DO178C : Many methods for predicting software reliability based on developmental metrics have been published, for example, software structure, defect detection rate, etc. This document does not provide guidance for those types of methods, because at the time of writing, currently available methods did not provide results in which confidence can be placed.)*.
- ✦ For this reason, the software reliability is demonstrated through a process application. The higher the software DAL\*\* (IDAL), the higher the effort required by the process application : *(extract from ARP4754A : Errors are mitigated by implementation of a Development Assurance. Development Assurance Process establishes confidence that system has been accomplished in a sufficiently disciplined manner to limit the development errors that could impact aircraft safety.)*

\* Item Development Assurance Level

\*\* Development Assurance Level

# Some properties of the processus

- ✦ It is a very formal processus (see Annex A1)
  - The **software development** processes (**requirements, desing, coding, integration**) that are defined in the Software Development Plan (**SDP**).
  - The integral processes that ensure the correctness and control of, and confidence in the software life cycle processes and their outputs
- ✦ More than 10 documents per item are needed to follow it (see Annex A2)
  - Software plans
  - Software standards
  - Software specification
  - ...
- ✦ 10 tables are managed to qualify each item (See Annex A3)
- ✦ Configuration Management issue (See Annex A5)
- ✦ Quality Assurance issue : An independant quality Assurance shall be performed in order to assess that the plans/standards/procedures are correctly applied.

# The software DAL

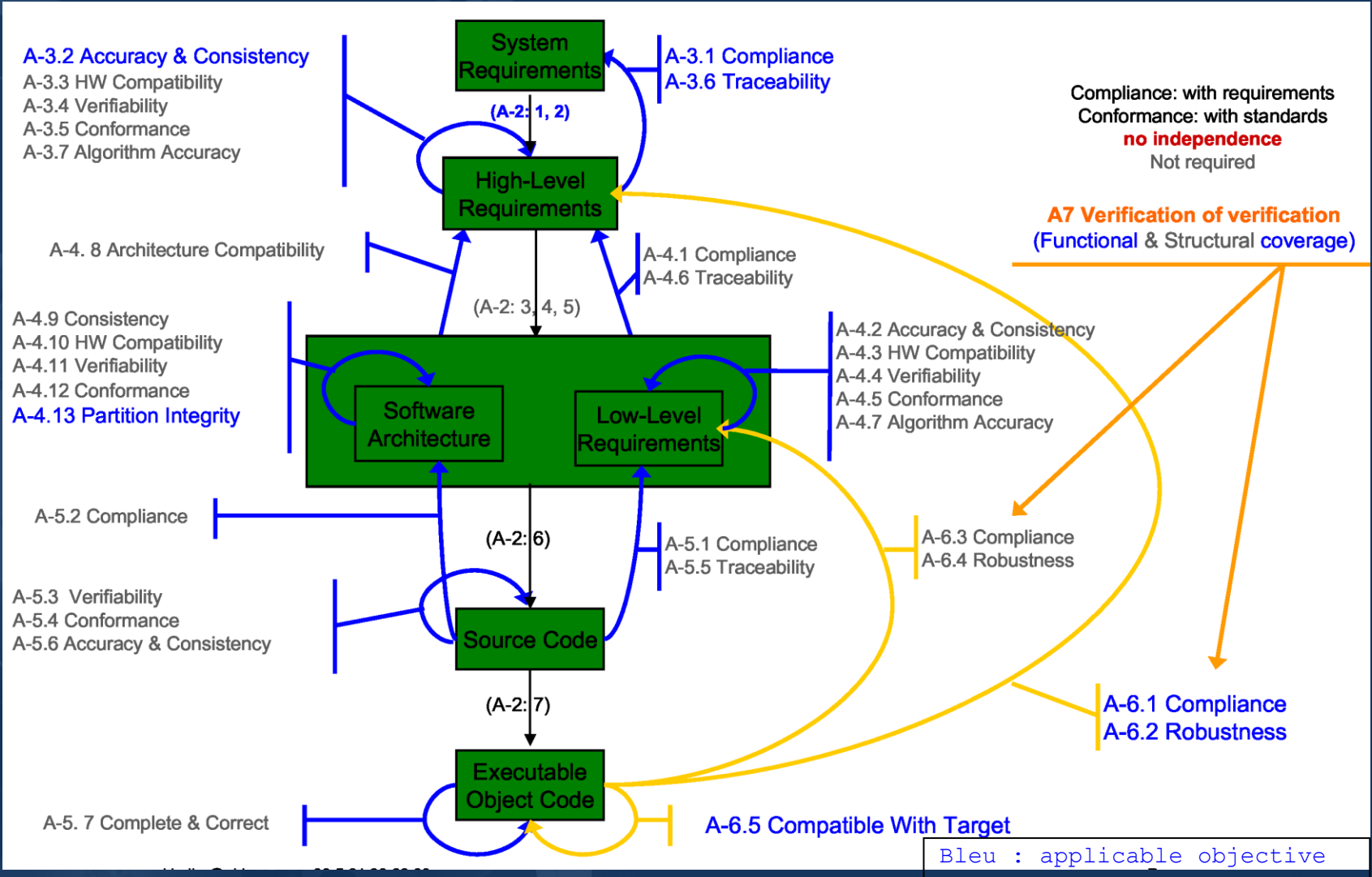
There are 4 DAL levels (IDAL in ARP4754A). The higher is the DAL, the higher is the verification effort. In a very simple way, the IDAL definition is :

- ✘ DAL D : The software item is considered as a black box. There is only one level of specification (High level Requirements (HLR)). The verification of the compliance/traceability of the HLR against the System Requirements allocated to the software. The test cases are written against the HLR. The binary is executed on the target for verifying its compliance to the HLR and its robustness.
- ✘ DAL C : DAL D + The software item is considered as a white box. The detailed definition (Low Level Requirements (LLR)) is required with the verification of the compliance/traceability of the LLR against the HLR and source code against the LLR. The structural coverage of the source code is at statement level. The binary is executed on the target for verifying its compliance to the HLR and LLR and its robustness.
- ✘ DAL B : DAL C + compatibility target + independence for some verification activities + structural coverage at decision level
- ✘ DAL A : DAL B + higher level of independence for verification activities + structural coverage at MC/DC + traceability demonstration between object code and source code.



# Software verification objectives

## DAL D software

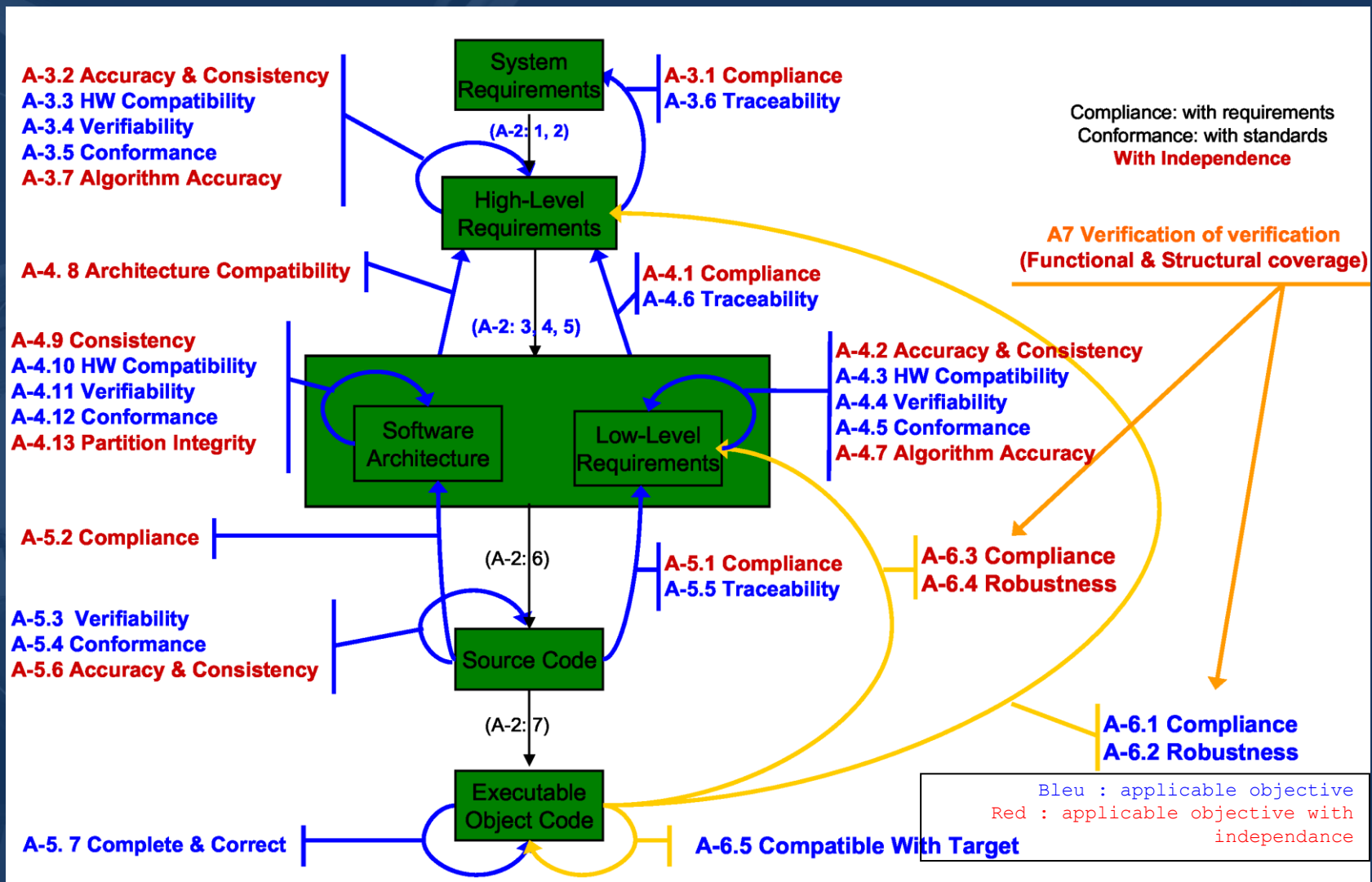


Bleu : applicable objective



# Software verification objectives

## DAL D software



# Tests based\*

---

DO178C verification means are reviews, analysis, tests and simulations (that have been added by the model based supplement DO331).

There are a lot of reviews (plans, standards, procedures, HLR, architecture, LLR, code, integration, verification results, ...) that means a lot of minutes and checklists that have to be managed in configuration.

Most of DO178C verification activities still rely on tests even if a lot of progress have been performed by enabling the usage of the formal methods and simulations for some dedicated verifications.

- ✘ Tests: several objectives of the table A6 required to perform tests on the target at least for the HLR test cases.
- ✘ Structural coverage is computed after executing requirements based tests (It is a way for identifying unintended function).

\* Some possible activities described in Annex A4

# Tools qualification issue

- ✦ If the tool eliminates, reduces, or automates processes of DO178C and if its outputs are not verified as specified in section 6 of DO178C, **the tool shall be qualified** at the Tool Qualification Level (TQL) defined by table in Annex A7. Five levels of tool qualification (TQL-1 to TQL-5) are identified based on the tool use and its potential impact in the software life cycle processes. TQL-1 is the most rigorous level and TQL-5 is the least rigorous level. When assessing the impact of a given tool, the criteria should be considered sequentially from criteria 1 to criteria 3 (see Annex A6).
- ✦ For example, for a TQL-1 tool (e.g. a code generator used at DAL A), the tool shall be developed with a level of rigor very close to the DAL A of DO178C. DO330 defines the tool qualification objectives.

# Certification liaison issue

- ✦ The certification process at software level is driven by DO178C and possibly several dedicated CRIs (Certification Review Items) / IPs (Issue Papers).
  
- ✦ ***It is submitted to the acceptance of a governmental Agency (EASA for Europe/FAA for US) thanks to specific audits (see FAA order 8110.49):***
  - Approval of the "Applicant" software plans
    - ✦ Audit SOI1 "planning review"
  - Approval of the compliance with applicable requirements of the "Applicant" software development data
    - ✦ Audit SOI2 "development review"
    - ✦ Audit SOI3 "verification review"
  - Approval of the "Applicant" Software Accomplishment Summary describing any deviations from the software plans and open problem reports
    - ✦ Audit SOI4 "final review"

SOI : Stage of Involvement



# En guise de conclusion – Domaine Aéronautique

## ✦ LA CONTRAINTE DE SÉCURITÉ ENTRAINE DES EXIGENCES SUR :

### ➔ L'ORGANISATION MISE EN PLACE

- ✦ DÉVELOPPEURS
- ✦ MANAGEURS

### ➔ LES MÉTHODES DE DÉVELOPPEMENT

- ✦ DU MATÉRIEL
- ✦ ET DU LOGICIEL
- ✦ Y COMPRIS LES OUTILS UTILES AU DÉVELOPPEMENT

### ➔ LA DIFFÉRENCIATION DES RÔLES À TOUS LES NIVEAUX

- ✦ L'ÉLABORATION DES RÈGLEMENTS
- ✦ L'ÉLABORATION DES STANDARDS INDUSTRIEL
- ✦ LE DÉVELOPPEMENT
- ✦ LA RESPONSABILITÉ DE L'ACCEPTATION

## ✦ LE PROCESSUS EST TRÈS LONG

## ✦ LE PROCESSUS EST TRÈS COUTEUX

## ✦ PAS DE PRISE EN COMPTE DES APPROCHES MACHINE LEARNING

## ✦ CE PROCESSUS EST INDISPENSABLE A GARANTIR LES NIVEAUX DE SÉCURITÉ CIBLÉS

# Agenda

- ✦ VÉRIFICATION, VALIDATION ET CERTIFICATION
- ✦ QUELQUES CHIFFRES
- ✦ LE MONDE AÉRONAUTIQUE
- ✦ LE MONDE AUTOMOBILE
- ✦ ET LE MONDE ROBOTIQUE ?
- ✦ *UNE CONCLUSION ?*



BIENVENUE À LA JOURNÉE D'ÉTUDES  
SIA VALIDATION DU VÉHICULE AUTONOME

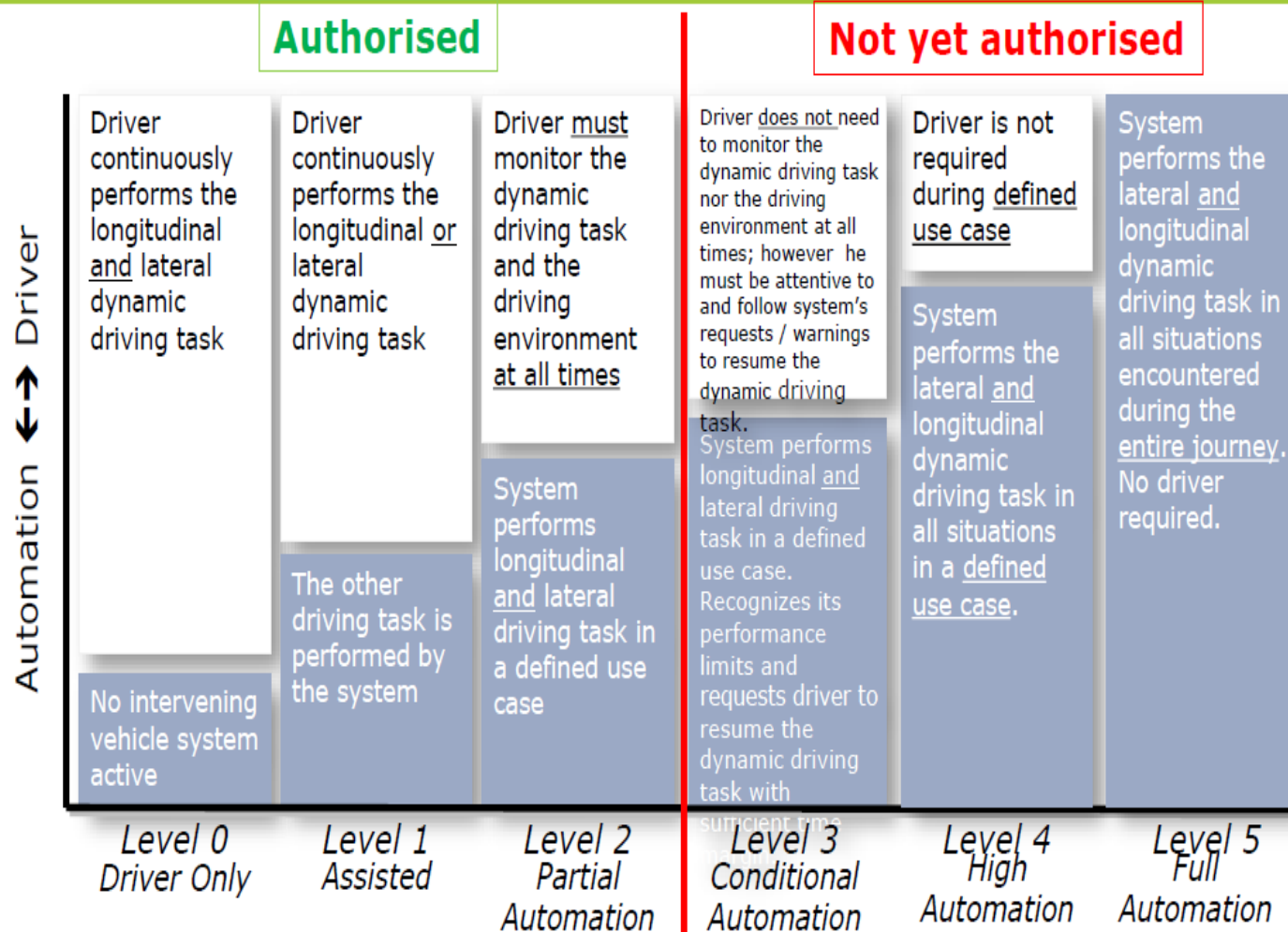
Journée d'Etude et Exposition  
**SIA VALIDATION DU VÉHICULE AUTONOME**  
Combinaison de simulations numériques,  
d'essais et de tests en simulateurs

MAISON DES TRAVAUX  
PUBLICS - FNTP  
PARIS, FRANCE  
27 JUIN  
2018



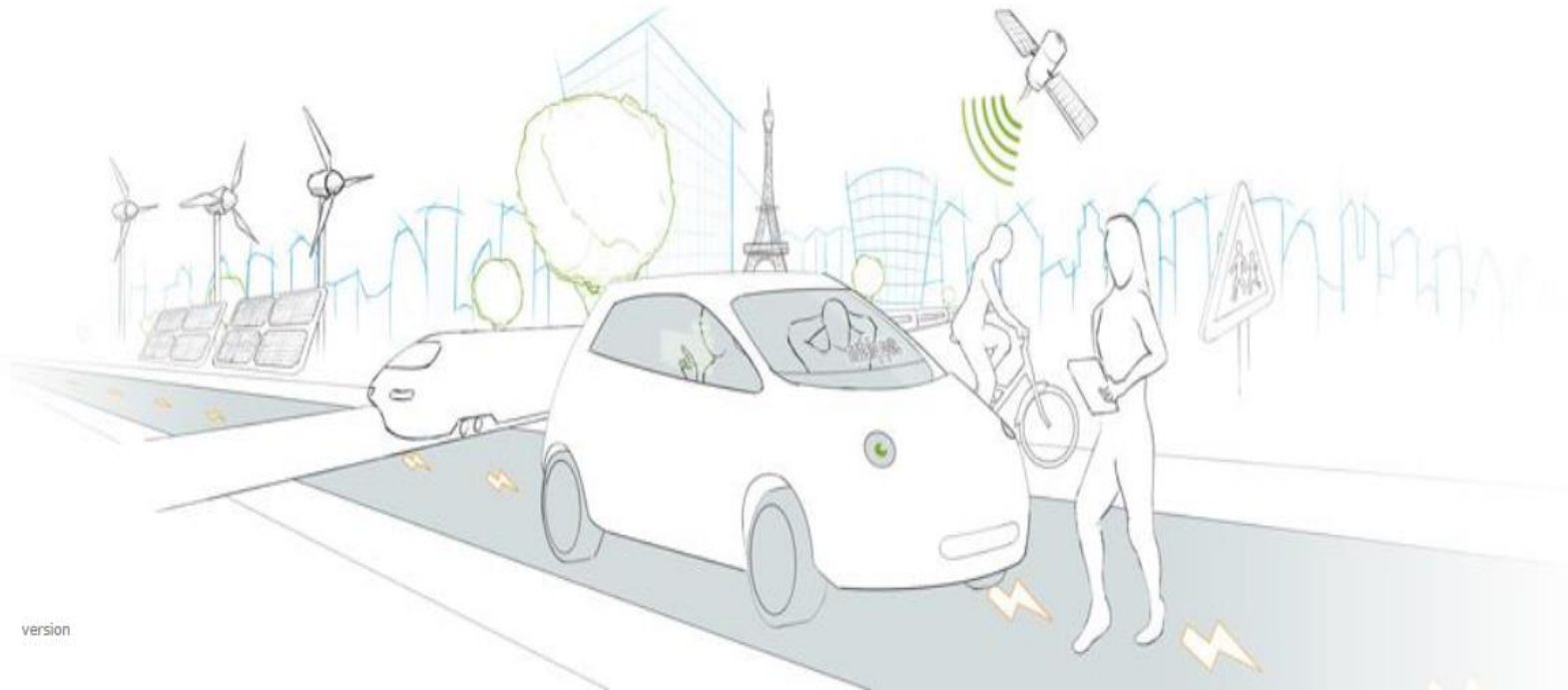
SIA SOCIÉTÉ DES INGÉNIEURS DE L'AUTOMOBILE

# TRUE AUTOMATION STARTS FROM LEVEL 3 (SAE)



\*terms acc. to SAE J3016

# THE MAJOR STAKE IS SAFETY





# ADAS

(L1, L2, L3)



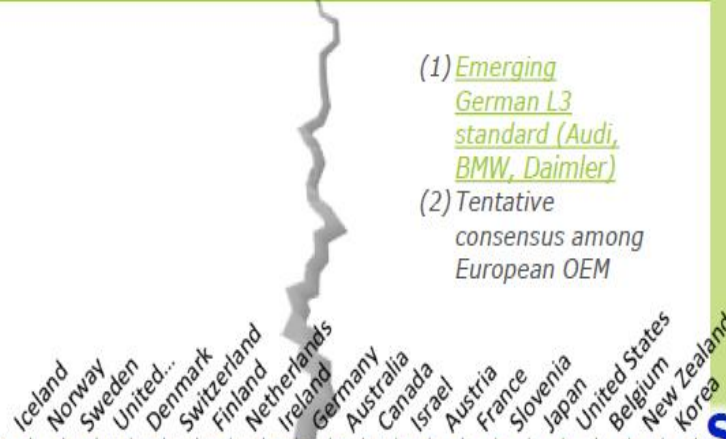
**Driver** is the last resort

**Driver** reliability proof



Driver training + experience

# AD : A MAJOR DISRUPTION



OECD average fatalities per hour

All roads:  $10^{-6}$

Highways:  $10^{-7}$

AD objective:  $10^{-8}$  (2)

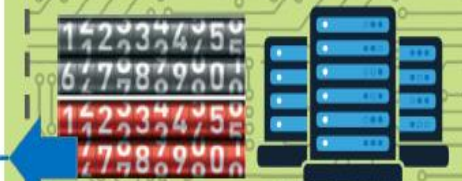
# AD

(L3+<sup>(1)</sup>, L4, L5)



**System** is the last resort

**System** reliability proof



Massive mile accumulation + resimulation

# SAFETY DEVELOPMENT AND VALIDATION

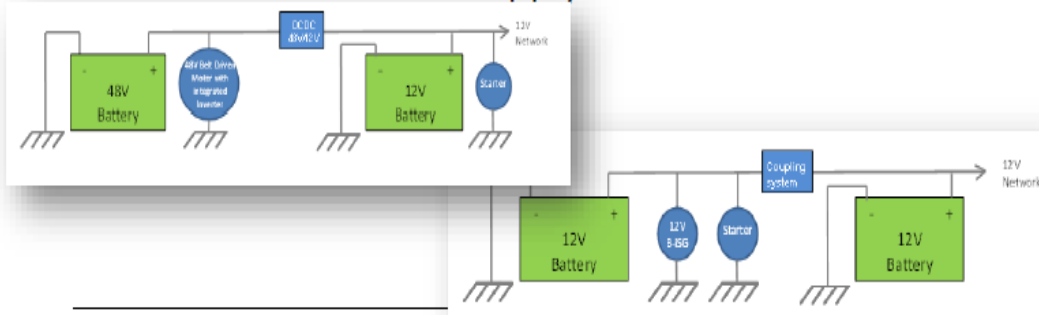
ISO 26262 defines how to assess a risk and the need for each step:

- ❖ System
- ❖ Software
- ❖ Hardware
- ❖ Production...

1. Vocabulary		
2. Management of functional safety		
2-4 Overall safety management	2-4 Safety management during the concept phase and the product development phases	2-7 Safety management during production, operation, service and decommissioning
3. Concept phase	4. Product development at the system level	5. Production, operation, service and decommissioning
3-5 Item definition	4-4 Setting topics for the product development at the system level	5-4 Safety analysis
3-6 Hazard analysis and risk assessment	4-4 Technical safety concept	4-4 System problem integration and verification
3-7 Functional safety concept	4-7 System architectural design	5-4 Planning for production, operation, service and decommissioning
		5-4 Production
		5-7 Operation, service and decommissioning
12. Adaptation of ISO 26262 for motorcycles	6. Product development at the hardware level	6. Product development at the software level
12-4 Safety cutouts	6-4 General topics for the product development at the hardware level	6-4 General topics for the product development at the software level
12-6 Confirmation measures	6-4 Specification of electromagnetic safety requirements	6-4 Specification of software safety requirements
12-7 Hazard analysis and risk assessment	6-7 Hardware design	6-7 Software architectural design
12-8 Vehicle integration and testing	6-8 Evaluation of the hardware architecture	6-8 Software unit design and implementation
12-8 Safety validation	6-8 Evaluation of the safety of systems due to random hardware failures	6-8 Software unit verification
	6-10 Hardware integration and verification	6-11 Testing of the produced software
8. Supporting processes		
8-5 Interfaces within distributed developments	8-8 Verification	8-14 Proven in use argument
8-5 Specification and management of safety requirements	8-10 Configuration management	8-18 Identifying a base vehicle or item in an application out of scope of ISO 26262
8-7 Configuration management	8-11 Confidence in the use of software tools	8-18 Integration of safety-related systems not developed according to ISO 26262
8-8 Change management	8-12 Qualification of software components	
	8-13 Evaluation of hardware elements	
9. ASIL-oriented and safety-oriented analyses		
9-4 Requirements decomposition with respect to ASIL factoring	9-7 Analysis of dependent failures	
9-4 Criteria for consistency of elements	9-8 Safety analysis	
10. Guideline on ISO 26262		

## Redundancy for Autonomous Driving:

- ❖ Redundant Sensors & Actuators
- ❖ Redundant Communication Networks
- ❖ Redundant Power supply Networks



## Additional Safety Stakes:

- ❖ For Autonomous Driving, Automotive EE Architecture has to switch from Fail Safe design to Fail Operational.
- ❖ Safety has also to consider SOTIF (Safety of the Intended Functionality)

- **AD success depends on societal acceptance**
- **Safety is the biggest stake**
- **E/E architecture is the first lever**
- **Validation by combination of data collection and simulation are the second lever.**

# En guise de conclusions – Domaine Automobile

- ✦ L'AVÈNEMENT DU VÉHICULE AUTONOME AMÈNE À RENFORCER LES EXIGENCES
- ✦ LE MONDE AUTOMOBILE SE RAPPROCHE DU MONDE AÉRONAUTIQUE
- ✦ LE PROCESSUS AÉRONAUTIQUE ACTUEL EST TROP LONG ET TROP COUTEUX
- ✦ APPLICATION AU DOMAINE AUTOMOBILE – VÉHICULE AUTONOME
  - ➔ LA NATURE DU MARCHÉ EST TRÈS DIFFÉRENTE (~40000 / >> MILLIONS)
  - ➔ NÉCESSITÉ DE DIMINUER LE TEMPS ET LES COÛTS
  - ➔ POSSIBILITÉ PAR L'AMORTISSEMENT DE L'INVESTISSEMENT ?
  - ➔ MISE EN PLACE EN COURS D'AUTORITÉS INDÉPENDANTES (CERTIFICATION ET OPÉRATION)
  - ➔ CERTIFICATION HORS AUTO-CERTIFICATION PROBABLEMENT À VENIR
- ✦ OPPORTUNITÉS POUR LES AUTRES DOMAINES ?



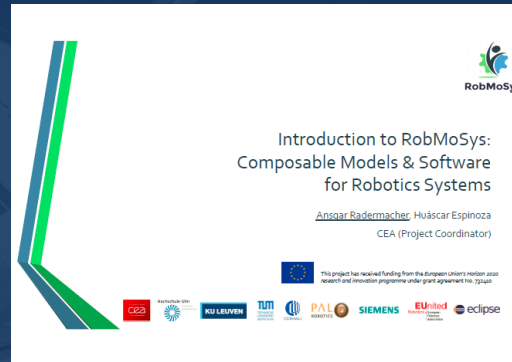
# Agenda

---

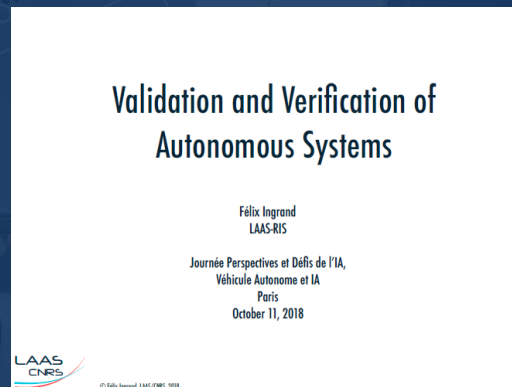
- ✦ VÉRIFICATION, VALIDATION ET CERTIFICATION
- ✦ QUELQUES CHIFFRES
- ✦ LE MONDE AÉRONAUTIQUE
- ✦ LE MONDE AUTOMOBILE
- ✦ ET LE MONDE ROBOTIQUE ?
- ✦ *UNE CONCLUSION ?*

# Les Initiatives en cours – Deux exemples

## ✦ LES APPROCHES « MODEL-DRIVEN » : LE PROJET EUROPÉEN RobMoSys



## ✦ LES TRAVAUX DE VÉRIFICATION / VALIDATION : PROPOSITIONS LAAS



## ✦ AXES DE RECHERCHE - PROPOSITIONS : POINT DE VUE

# RoMoSys in a Nutshell



- **RobMoSys**: Composable Models and Software for Robotic Systems
- In response to **H2020** Project – ICT-26- TOPIC : System abilities, development and pilot installations
- SubTopic c: **Innovation Action** on systems development technology.

The "System development tools" sub-call

- **Start Date** 01/01/2017
- **End Date** 31/12/2020
- **Duration** 4 Years
- **Budget** 8M, where 4 M for Open-Calls
- **Web Site** <http://robmosys.eu/>



# Complexity in SW Robotics



Application



Algorithms



Stakeholders

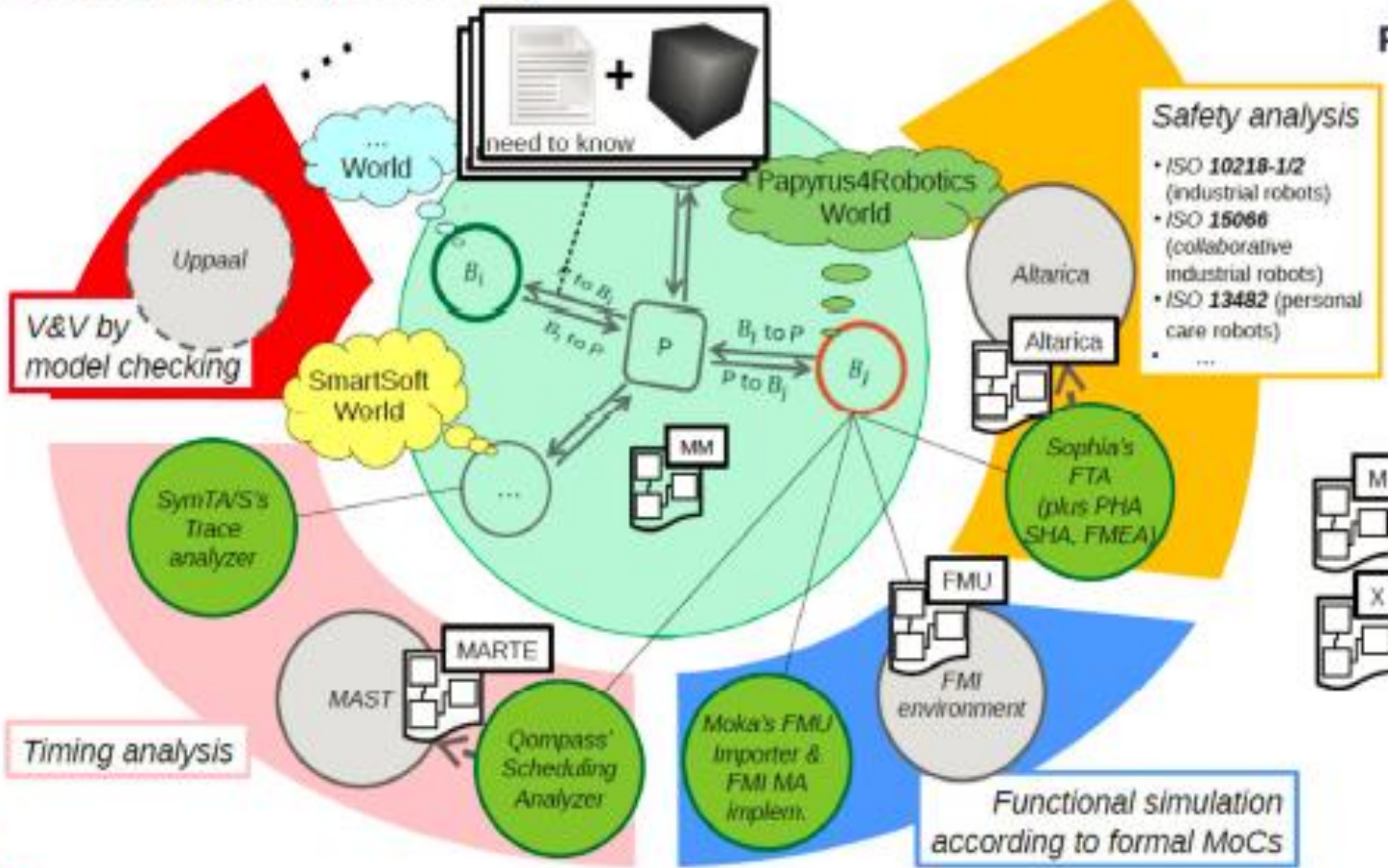


Platforms

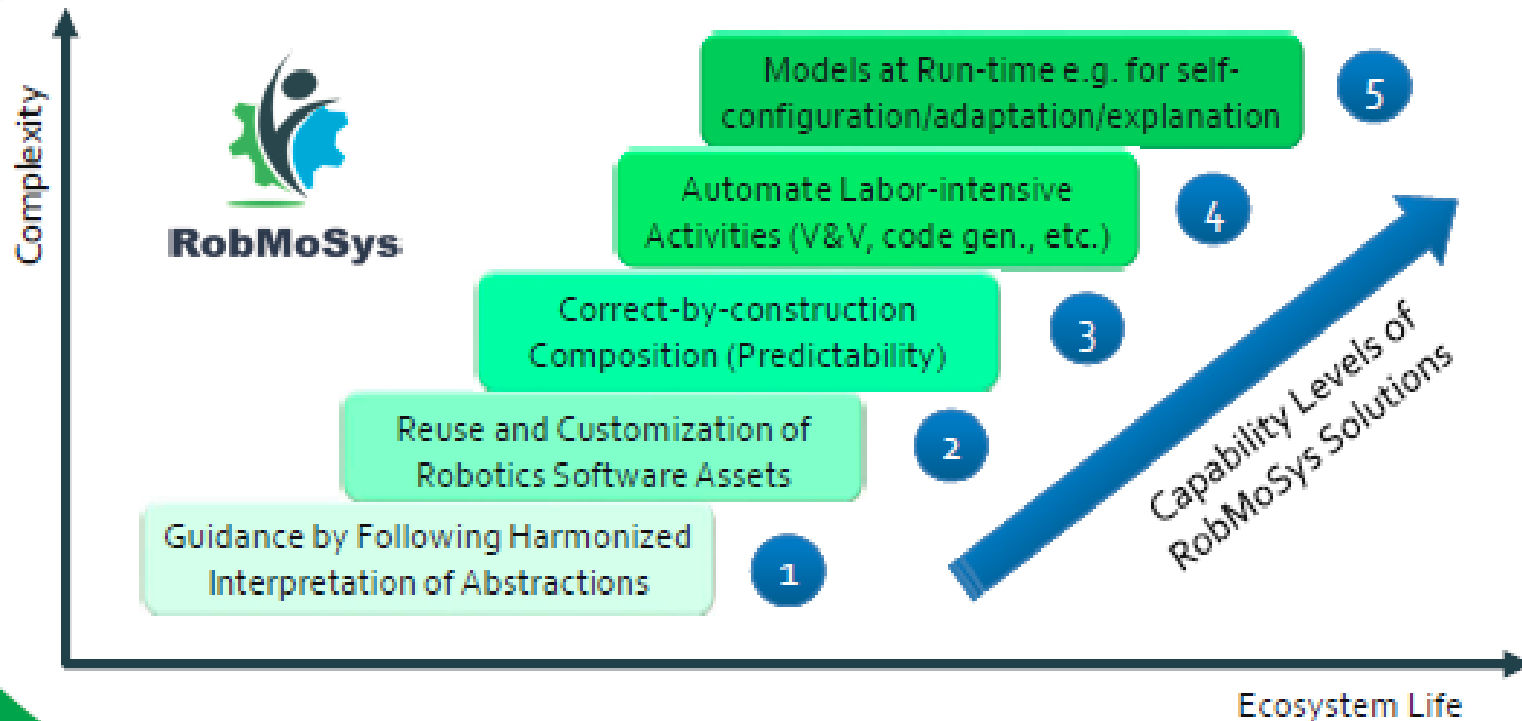




# Tooling Interoperability

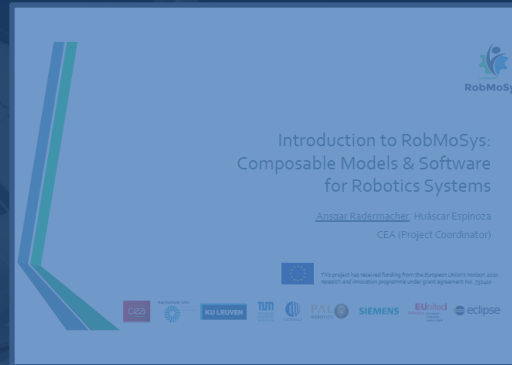


# Ambition of Creating Models

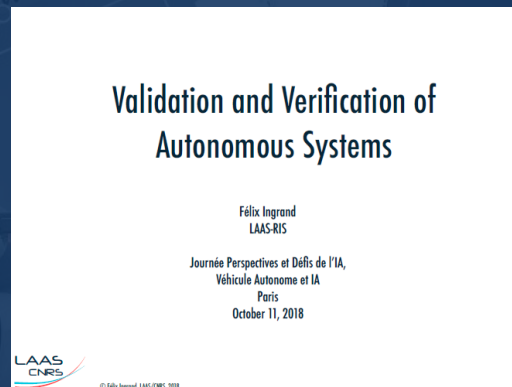


# Les Initiatives en cours – Deux exemples

## ✦ LES APPROCHES « MODEL-DRIVEN » : LE PROJET EUROPÉEN ROBMoSYS



## ✦ LES TRAVAUX DE VÉRIFICATION / VALIDATION : PROPOSITIONS LAAS



## ✦ ORIENTATIONS : PROPOSITIONS

# Software Validation and Verification

- [ Require formal models and mathematically/logically sound “checking” techniques
  - formal models (e.g., FSM, IO automata, Petri nets, timed automata, situation calculus, synchronous systems, etc)
  - checking by reachable state exploration (e.g. model checking), logical induction (e.g. theorem proving, sat solving, etc) or runtime verification
  - complete methods, over approximation, statistical methods, etc...



# V&V models: Different situations over a complete autonomous system

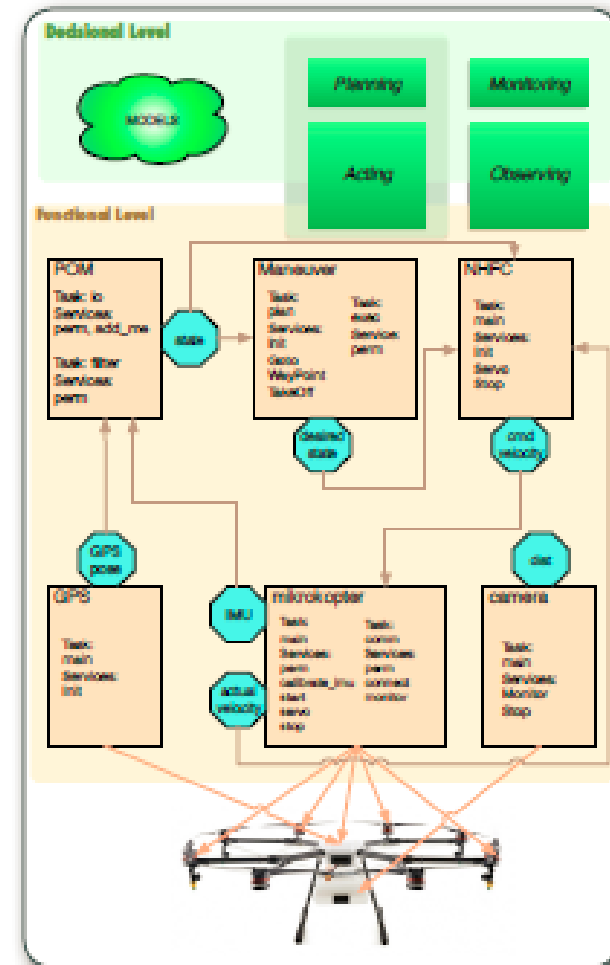
**Formal models:** decisional : planning (e.g. UPPAAL, model checking), monitoring, FDIR, observing

**Learned models:** Reinforcement learning models, perception models, etc.

**Specification models:** Software engineering models: e.g. GenoM3, Oroccos, MAUVE, RobotML, etc.

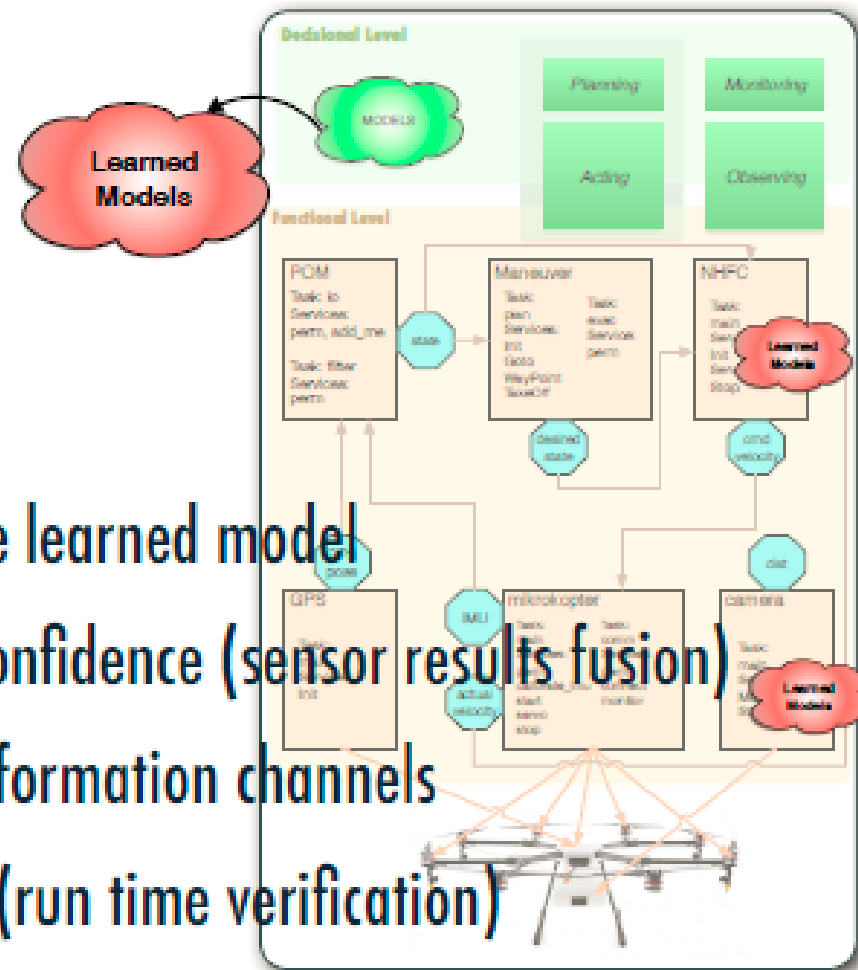
**Programming directly the Model:** Orccad, Scade, etc.

No Model...



# V&V of learned models...

- [ Machine learning is the new AI...
- [ Hard to extract a formal model... but we should try
- [ Proper environment modeling
- [ Properly characterize the bound of the learned model
- [ Use multiple sources to improve the confidence (sensor results fusion)
- [ Consistency checking over different information channels
- [ Safety bag around these components (run time verification)



- [1] D. Amodei, C. Olah, J. Steinhardt, P. Christiano, J. Schulman, and D. Mané, "Concrete Problems in AI Safety," arXiv.org, 1606.06565v2, vol. cs.AI, 21-Jun-2016. <http://arxiv.org/abs/1606.06565v2>
- [2] S. A. Seshia, D. Sadigh, and S. S. Sastry, "Towards Verified Artificial Intelligence," arXiv.org, 1606.08514v3 vol. cs.AI, 28-Jun-2016. <http://arxiv.org/abs/1606.08514v3>

# Conclusion

- [ When there are models... there is hope!
- [ Adapt the model and the V&V techniques
- [ Try to keep the overall consistency
- [ AI components are mostly OK (wrt formal V&V)
- [ V&V and certification of “learned model” based components remain a challenge

# Des axes de recherche\*

## Point de vue : Vérification, Validation et Certification

### RESEARCH QUESTION 1

How can **knowledge-driven** (“affordance-based”) **robot programming, perception and learning** be made more **realtime**, while still taking into account more **prior knowledge** about the tasks, the robots, the objects they interact with, and the environment they have to survive in?

### RESEARCH QUESTION 3

Which new design paradigm can provide **cheap, light and safe** (hence, “lousy”) robot hardware? This is a necessary evolution before **robotics platforms** can become **acommodity**.

### RESEARCH QUESTION 5

What is the **essential and minimal structure** to model the **functional aspects** of robotic systems

### RESEARCH QUESTION 2

What are the formal Domain Specific Languages (DSLs) that can make the **knowledge representation** (and hence the **programming** of robots) a lot more easy? And, at the same time, a lot more *semantically consistent*, and (hence!) *deterministic*, and (hence?) *verifiable*, and (hence!) **certifiable**, and (hence?) societally **trustworthy**.

### RESEARCH QUESTION 4

What is the **essential and minimal structure** to model the **software aspects** of robotic systems?

How should **robot control software** be developed in the future?

What **architectural patterns** can help us cope with the **exploding complexity** in knowledge, task variations, and distribution over several sub-systems?

### RESEARCH DRIVER 1

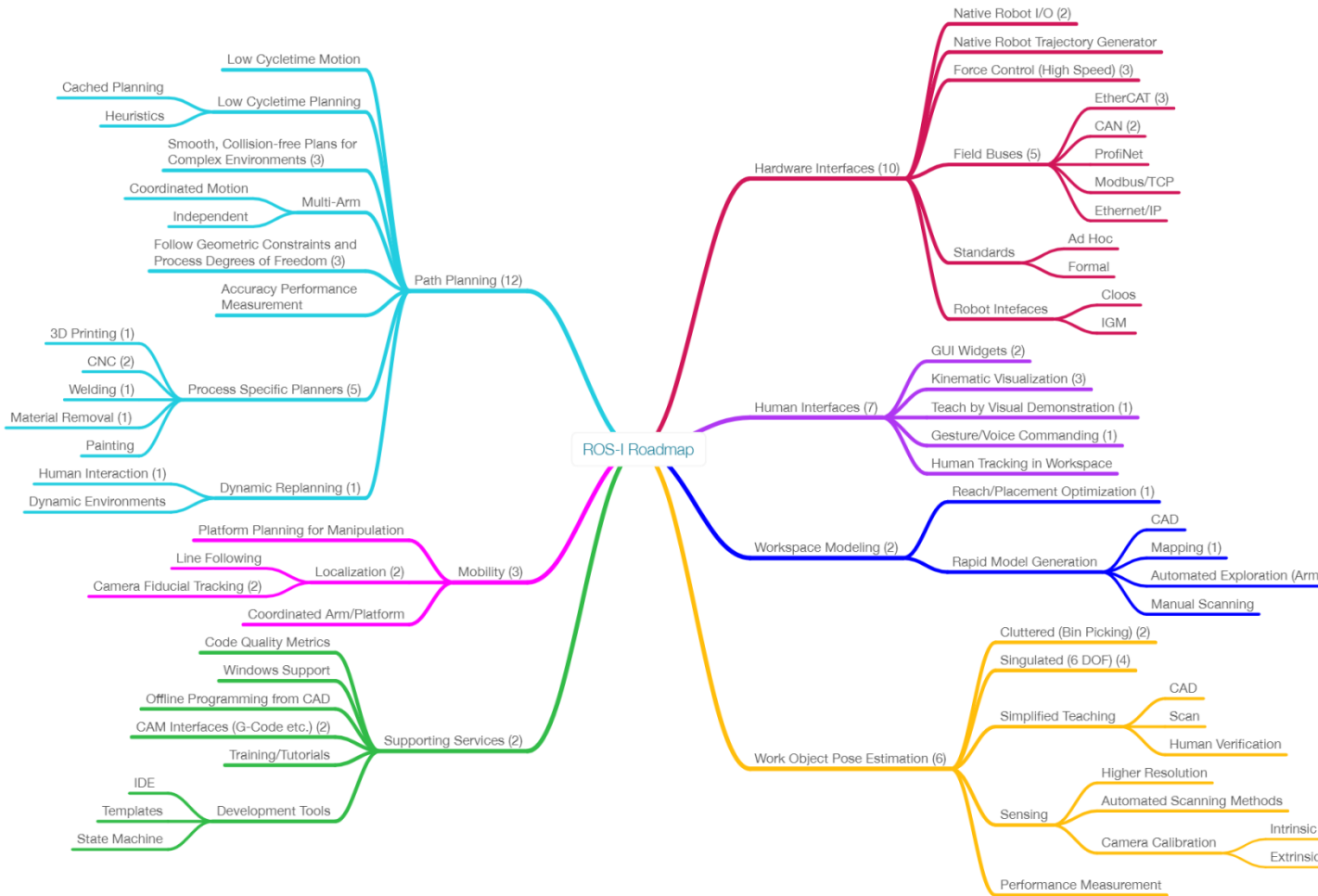
*The societal expectation to have access to (only) trustworthy robots*

### RESEARCH DRIVER 2

*The societal expectation to have trustworthy Artificial Intelligence*



# Des axes de recherche et ROS ?



Version de 2014 la plus récente (?)

# Agenda

---

- ✦ VÉRIFICATION, VALIDATION ET CERTIFICATION
- ✦ QUELQUES CHIFFRES
- ✦ LE MONDE AÉRONAUTIQUE
- ✦ LE MONDE AUTOMOBILE
- ✦ ET LE MONDE ROBOTIQUE ?
- ✦ *UNE CONCLUSION ?*

# Tentative de conclusion générale

- ✦ LA SÉCURITÉ IMPLIQUE DES DÉVELOPPEMENTS COUTEUX ET LONG
- ✦ DES HAUTS NIVEAUX DE SÉCURITÉ IMPLIQUENT UNE SÉPARATION STRICTE DES RÔLES
- ✦ DES HAUTS NIVEAUX DE SÉCURITÉ IMPLIQUENT LA MAÎTRISE DE TOUTE LA PILE LOGICIELLE (OS/MIDDLEWARE/COMPOSANTS)
  
- ✦ LA MONTÉE EN PUISSANCE DES VOITURES AUTONOMES PERMET D'ENVISAGER UNE RÉDUCTION DES COÛTS DES PROCESSUS DE DÉVELOPPEMENT
  - ➔ MONTÉE EN PUISSANCE DES APPROCHES MODÈLES
  - ➔ MONTÉE EN PUISSANCE DES GÉNÉRATEURS DE CODE
  
- ✦ L'APPROCHE MODÈLE FAVORISE LA MONTÉE EN PUISSANCE DES MIDDLEWARE
  
- ✦ LE DÉVELOPPEMENT DE ROS N'APPORTE PAS À CE JOUR LES GARANTIES NÉCESSAIRES
- ✦ LES ASPECTS VÉRIFICATION ET VALIDATION DOIVENT ÊTRE CONSIDÉRÉS PAR LES TRAVAUX DE RECHERCHE DÈS L'ORIGINE POUR FAVORISER LEUR UTILISATION
- ✦ LORS DE L'ÉTUDE D'UNE NOUVELLE MÉTHODE DE CONTRÔLE OU AUTRE IL EST NÉCESSAIRE DE RÉFLÉCHIR DÈS L'ORIGINE AUX MOYENS DE SA VÉRIFICATION ET DE SON CONTRÔLE

---

# QUELQUES QUESTIONS ?

# Annexes pour le curieux

---

- ✦ A1 - Very formal software processes
- ✦ A2 - A lot of documentation
- ✦ A3 - DO178C tables
- ✦ A4 - Examples of verification activities
- ✦ A5 - The tool qualification issue : criteria
- ✦ A6 - The tool qualification issue : TQL



# A1 - Very formal software processes

---

- ✦ DO178C considers different types of software processes :
  - ➔ The **software development** processes (**requirements, desing, coding, integration**) that are defined in the Software Development Plan (**SDP**).
  - ➔ The integral processes that ensure the correctness and control of, and confidence in the software life cycle processes and their outputs:
    - ✦ The **verification** process that is defined in the Software Verification Plan (**SVP**)
    - ✦ The **Configuration Management** that is defined in the Software Configuration Management Plan (**SCMP**)
    - ✦ The **Software Quality Assurance** that is defined in the Software Quality Assurance Plan (**SQAP**)
    - ✦ The **certification liaison** process that is defined in the Plan for Software Aspect of Certification (**PSAC**)

## A2 - A lot of documentation :

main outputs of the software processes

---

- ✘ Software plans : PSAC, SDP, SVP, SCMP, SQAP
- ✘ Software standards : software requirements, design, code
- ✘ Software specification (HLR)
- ✘ Software design including architecture (LLR)
- ✘ Source code
- ✘ Executable Object Code (EOC)
- ✘ Software test cases and procedures
- ✘ Software verification results (minutes of review, tests report, ...)
- ✘ Software Configuration Index (SCI)
- ✘ Software Configuration Management records
- ✘ Software Configuration Index (SCI)
- ✘ Software life cycle Environment Configuration Index (SECI)
- ✘ Problem Report (PR)
- ✘ Software Quality Assurance records
- ✘ Software Accomplishment Summary (SAS)

# A3 - DO178C tables

---

- ❏ DO178C defines software objectives thanks to 10 tables (see appendix A) that depend on the software IDAL.
  - ➔ table A-1 : Software planning process Some of the expected products
  - ➔ table A-2 : Software development processes
  - ➔ table A-3 : Verification of outputs of Software requirements process
  - ➔ table A-4 : Verification of outputs of Software design process
  - ➔ table A-5 : Verification of outputs of Software coding process & integration process
  - ➔ table A-6 : Verification of outputs of Software integration process
  - ➔ table A-7 : Verification of outputs of verification results
  - ➔ table A-8 : Software configuration management process
  - ➔ table A-9 : Software quality Assurance process
  - ➔ table A-10 : Certification liaison process



# A4 - Examples of verification activities

- ✳ The WCET (Worst Case Execution Time) shall be defined as the verification of the stack usage.
- ✳ MC/DC structural coverage shall be achieved for the DAL A software items.
- ✳ The data flow coverage
- ✳ The control flow coverage
- ✳ The integration tests
- ✳ The robustness test cases :
  - a) Real and integer variables should be exercised using equivalence class selection of invalid values.
  - b) System initialization should be exercised during abnormal conditions.
  - c) The possible failure modes of the incoming data should be determined, especially complex, digital data strings from an external system.
  - d) For loops where the loop count is a computed value, test cases should be developed to attempt to compute out-of-range loop count values, and thus demonstrate the robustness of the loop-related code.
  - e) A check should be made to ensure that protection mechanisms for exceeded frame times respond correctly.
  - f) For time-related functions, such as filters, integrators, and delays, test cases should be developed for arithmetic overflow protection mechanisms.
  - g) For state transitions, test cases should be developed to provoke transitions that are not allowed by the software requirements.

# A5 - Configuration Management issue

---

- ✦ All the outputs of the DO178C activities shall be managed in configuration depending on the of the software item DAL. Most of the time, the configuration management is performed thanks a Configuration Management tool such as Synergy, ...
- ✦ The configuration level of the software items depends on the DAL and the software item.
- ✦ The Baselines (The approved, recorded configuration of one or more configuration items) shall be defined.
- ✦ The modifications of some configurations items shall be defined thanks to formalized Problem Reports (PR). Most of the time, the PR are during a Change Control Board (CCB)



# A6 - The tool qualification issue :

## The tool criteria

---

### ➔ Software Tools Classification

- **Criteria 1 (Development Tool)** : A tool whose output is part of the resulting software and thus could insert an error. Example : code generator
- **Criteria 2** : A tool that automates verification process(es) and thus could fail to detect an error, and whose output is used to justify the elimination or reduction of:
  - Verification process(es) other than that automated by the tool,(Example, an analysis tool that is able to detect overflow/divisionby0/stackoverflow by analysis and the results of which are used to eliminate overflow/divisionby0/stackoverflow tests) or
  - Development process(es) (which could have an impact on the resulting software).
- **Criteria 3 (Verification Tool)** : A tool that, within the scope of its intended use, could fail to detect an error.

# A7 - The tool qualification issue :

## The Tool Qualification Level (TQL)

- ➔ The tool qualification objectives are defined thanks the tool criteria and the software Assurance Level.

DAL	criteria		
	1	2	3
A	TQL-1	TQL-4	TQL-5
B	TQL-2	TQL-4	TQL-5
C	TQL-3	TQL-5	TQL-5
D	TQL-4	TQL-5	TQL-5